

U.S. Department of Commerce, Patent and Trademark Office		Atty Docket No.	Serial No. (0817)
		M-15339 US	Not yet assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s)	
(Use several sheets if necessary)		Xiang Zhu; Ming Qu; Yongmin Ge	
		Filing Date	Group 2816
		Herewith 4/2/04	Not yet assigned

## U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
WT	AA 6,498,538	12/24/2002	Qu et al.	331	25	
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

## Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass		Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

WT	AQ	ISSCC 2003/Session 24/Clock Generation/Paper 24.1, 2003 IEEE International Solid-State Circuits Conference
WT	AR	"Differential CMOS Circuits for 622-MHz/933-MHz Clock And Data Recovery Applications," by Hormoz Djahanshahi et al., IEEE Journal of Solid-State Circuits, Vol. 35, No. 6, June 2000
	AS	

Examiner *Qu et al.* Date Considered *4/12/04*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.